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				Application Number	Not Yet Assigned
				Filing Date	March 3, 2004
				First Named Inventor	Satoru AKIYAMA
				Art Unit	2185
				Examiner Name	J. ROM
Sheet	1	of	1	Attorney Docket Number	500.43581X00

U.S. PATENT DOCUMENTS

## **FOREIGN PATENT DOCUMENTS**

Examiner Signature	<i>Dempsey</i>	Date Considered	10/24/06
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<p>Substitute for form 1449B/PTO</p> <p><b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b></p> <p>(use as many sheets as necessary)</p>				<b>Complete If Known</b>	
				<b>Application Number</b>	<b>Not Yet Assigned</b>
				<b>Filing Date</b>	March 3, 2004
				<b>First Named Inventor</b>	S. AKIYAMA et al
				<b>Group Art Unit</b>	2185
<b>Examiner Name</b>	TRM				
Sheet	2	of	2	Attorney Docket Number	500.43581X00

<b>OTHER PRIOR ART—NON PATENT LITERATURE DOCUMENTS</b>					
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s). <small>Publication city and/or country where published</small>			T <sup>2</sup>
DT		ATWOOD, Bryan et al, "SES0 Memory: A CMOS compatible with high density embedded memory technology for mobile application," 2002 Symposium on VLSI Circuit Digest of Technical Papers, pp. 182-185, (2000) USA.			12 pages
DT		LAI, Stefan et al, "OUM - A 180 nm nonvolatile memory cell element technology for stand alone and embedded applications," 2001 IEEE International Electron Devices Meeting Digest of Technical Papers, pp. 803-806 (2001), USA.			4 pages
DT		"Combination Memories" searched on the Internet on April 28, 2003 <URL: http://www.sharp-world.com/products/device/flash/cmclist.html>			1-3,
DT		HWANG, Chorng-Lii et al, "A 2.9ns Random Access Cycle Embedded DRAM with a Destructive-Read Architecture", 2002 Symposium on VLSI Circuits Digest of Technical Papers, pp. 174-175, USA.			

Examiner Signature	Denzeyum	Date Considered	11/10/06
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